

PATENT

Case Docket No. ASMEX.236A

Date: April 29, 2002

Page 1

In re application of : Raaijmakers et al
 App. No. : 09/658,784
 Filed : September 11, 2000
 For : LOADLOCK WITH
 INTEGRATED PRE-CLEAN
 CHAMBER
 Examiner : Rudy Zervigon
 Art Unit : 1763

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April 29, 2002

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Sir:

Transmitted herewith is an amendment in the above-identified application.

(X) An extension of time to respond for Two month(s) is hereby requested.

Time Extension Fee:

() one month (\$110 large entity)
 (X) two months (\$400 large entity)
 () three months (\$920 large entity)

The PTO did not receive the following
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The fee has been calculated as shown below:

CLAIMS AS FILED

CLAIMS REMAINING AFTER AMENDMENT	HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDITIONAL FEE
Total Claims 32 —	65	= 0 ×	\$18	= \$0
Independent Claims 9 —	8	= 0 ×	\$84	= \$84
If application has been amended to contain multiple dependent claim(s), then add			\$280	= \$0
Time Extension Fee				\$400.00
			TOTAL ADDITIONAL FEE FOR THIS AMENDMENT	\$484.00

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- Letter to Draftperson and Revised Drawings.
- Return prepaid postcard.
- A check in the amount of \$484.00 is enclosed.
- Charge \$ to Deposit Account No. 11-1410.
- Please charge any additional fees, including any fees for additional extension of time, or credit overpayment to Deposit Account No. 11-1410.

Gordon H. Olson

Gordon H. Olson
Registration No. 20,319
Attorney of Record

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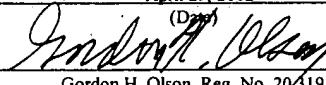
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant : Raaijmakers et al.) Group Art Unit: 1763
Appl. No. : 09/658,784)
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CHAMBER)
Examiner : R. Zervigon)

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Gordon H. Olson, Reg. No. 20319

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AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In response to the Office Action mailed November 28, 2001, Applicant respectfully submits the following amendments and comments in connection with the above-captioned application.

IN THE SPECIFICATION:

Please amend the paragraph starting at page 5, line 29, as follows:

With reference back to Figure 2A, a wafer carrier, which is indicated generally by the reference number 50, is attached to the elevator plate 26. Preferably, the wafer carrier 50 is sized and dimensioned to simultaneously support at least two wafers 37. Therefore, in the preferred embodiment, the wafer carrier 50 consists of a pair of supports or prongs 52a, 52b that extend generally upward from the lining 44. The prongs 52a, 52b are preferably secured to the elevator plate 26 by fasteners 54 that extend through the elevator plate 26 and the lining 44. Each prong 52a, 52b includes two arms 56, 57 that extend towards the center of the elevator plate 26. Together, the first set of arms 56 form a first shelf 56 for a wafer and the second set of arms 57 form a second shelf 57 for a wafer. Thus, as shown in Figure 2A, two wafers 37 can simultaneously rest on the first and second shelves 56, 57 of the wafer carrier 50.

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